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Patent, Trademark and Copyright Causes,
 Unfair Competition, Trade Secrets,
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*Practice limited to Matters and Proceedings
 before Federal Courts and Agencies; not Admitted
 in VA

**Registered Patent Agent; not Admitted in VA

FACSIMILE COVER SHEET

TO: United States Patent and Trademark Office
 PATENT PROSECUTION HIGHWAY PILOT PROGRAM
 Attn: Mano Padmanabhan
 Facsimile: 571-273-4210

Date: November 14, 2007

From: THE NATH LAW GROUP, U.S. Filing Dept.
 Customer no. 20529

RE: U.S. Serial Number 10/527,820
 Title: ACCESS METHOD
 Inventor: TAKAGI
 Our reference Number: 26642U

NO. OF PAGES (including this page): 32

COMMENTS AND/OR SPECIAL INSTRUCTIONS:

Response to Decision on Request to Participate in Patent Prosecution
 Highway Pilot Program and Petition to Make Special under 37 C.F.R.
 1.102(d).

Please direct any questions to Jerry Meyer (telephone: 703-548-6284).

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GMN /few)

Attorney Docket No. 26642U

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

TAKAGI

Serial No: 10/527,820

Group Art Unit: 2136

Filed: May 11, 2005

Examiner: MOAZZAMI, N. G.

For: **ACCESS METHOD****RESPONSE TO DECISION ON REQUEST TO PARTICIPATE IN PATENT
PROSECUTION HIGHWAY PILOT PROGRAM AND PETITION TO MAKE SPECIAL
UNDER 37 C.F.R. 1.102(d)**

PATENT PROSECUTION HIGHWAY PILOT PROGRAM
Attn: Magdalen Greenlief
Facsimile: 571-273-0125

This is in response to the Decision on Request to Participate in Patent Prosecution Highway Pilot Program and Petition to Make Special under 37 C.F.R. 1.102(d) dated October 24, 2007. The one month period for response is set to expire November 24, 2007. Accordingly, this Response is timely filed within the time period set by the Examiner.

CORRECTION OF DEFICIENCIES

The following reasons cited for the dismissal of the request for participation in the Patent Prosecution Highway Pilot Program are summarized below:

USSN 11/463,573

ABRAM

Page 2 of 2

1) The request is incomplete because Applicant has not provided a "correspondence of claims" table, and has not provided authorization for charging Petition fee. It seems that the office is not in receipt of Page 2 of the Request for Participating in the Patent Prosecution Highway.

2) Applicant has failed to submit a copy of the allowable/patentable claims in Japanese, and a statement that the claims in English are an accurate translation of the allowed Japanese claims are missing in the petition.

3) Applicant has failed to provide a Japanese language document of the reasons for refusal.

The omission of Page 2 of the original application appears to have been an inadvertent transmission error. Accordingly, a copy of the original application for Request for Participation in the PPH Pilot Program is resubmitted, along with a copy of the Japanese language document of the Reasons for Refusal.

Respectfully submitted,
THE NATH LAW GROUP

By: 

Gary M. Nath
Registration No. 26,965
Jerald L. Meyer
Registration No. 41,194
Customer No. 20529

Date: November 14, 2007**THE NATH LAW GROUP**

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GMN/JBG/few\C7994A2 ROA.doc

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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
Pro's B/zo (01-07)

REQUEST FOR PARTICIPATION IN THE PATENT PROSECUTION HIGHWAY (PPH) PILOT PROGRAM
BETWEEN THE JPO AND THE USPTO

Application No.:	10/527,820	First Named Inventor:	Yoshihiko TAKAGI
Filing Date:	July 15, 2004	Attorney Docket No.:	26642U
Title of the Invention:	ACCESS METHOD		

THIS REQUEST FOR PARTICIPATION IN THE PPH PILOT PROGRAM MUST BE FAXED TO:
THE OFFICE OF THE COMMISSIONER FOR PATENTS AT 571-273-0125 DIRECTED TO THE ATTENTION OF MAGDALEN GREENLIEF

APPLICANT HEREBY REQUESTS PARTICIPATION IN THE PATENT PROSECUTION HIGHWAY (PPH) PILOT PROGRAM AND PETITIONS TO MAKE THE ABOVE-IDENTIFIED APPLICATION SPECIAL UNDER THE PPH PILOT PROGRAM.

The above-identified application validly claims priority under 35 U.S.C. § 119(a) and 37 C.F.R. 1.55 to one or more corresponding JPO application(s).

The JPO application number(s) is/are: 2003-275672, 2004-197453 and 2006-341650

The filing date of the JPO application(s) is/are: July 16, 2003, July 2, 2004 and December 19, 2006

1. List of Required Documents:

- a. A copy of all JPO office actions excluding "Decision to Grant a Patent") in the above-identified JPO application(s).
- ☒ Is attached.
- ☐ Is available via Dossier Access System. Applicant hereby requests that the USPTO obtain these documents via the Dossier Access System.
- *It is not necessary to submit a copy of the "Decision to Grant a Patent" and an English translation thereof.
- b. A copy of all claims which were determined to be patentable by the JPO in the above-identified JPO application(s).
- ☒ Is attached.
- ☐ Is available via Dossier Access System. Applicant hereby requests that the USPTO obtain these documents via the Dossier Access System.
- c. English translations of the documents in a. and b. above along with a statement that the English translations are accurate are attached.
- Information disclosure statement listing the documents cited in the JPO office actions is attached.
- Copies of all documents are attached except for U.S. patents or U.S. patent application publications.

[Page 1 of 2]

This collection of information is required by 35 U.S.C. 119, 37 CFR 1.55, and 37 CFR 1.102(d). The information is required to obtain or retain a benefit by the public, which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37CFR 1.11 and 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. FAX COMPLETED FORMS TO: Office of the Commissioner for Patents at 571-273-0125, Attention: Magdalen Greenleaf.

Pro/ST/20 (01-07)
 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
 Under the Framework Regulation Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

REQUEST FOR PARTICIPATION IN THE PATENT PROSECUTION HIGHWAY (PPH) PILOT PROGRAM BETWEEN THE JPO AND THE USPTO

(continued)

Application No.:	10/527,820	First Named Inventor:	Yoshihiko TAKAGI
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II. Claims Correspondence Table:

Claims in US Application	Patentable Claims in JP Application	Explanation regarding the correspondence
1-13	1-13 The JPO application number is 2006-341650.	Each claim in the U.S. application is based upon and corresponds to the similarly allowed claims of the Japanese patent except for possible changes to improve clarity, grammar, and syntax.

III. All the claims in the US application sufficiently correspond to the patentable/allowable claims in the JPO application.

IV. Payment of Fees:

The Commissioner is hereby authorized to charge the petition fee under 37 CFR 1.17(h) as required by 37 CFR 1.102(d) to

☒ Deposit Account No. 14-0112
☐ Credit Card. Credit Card Payment Form (PTO-2038) is attached.

Signature	Date
Name (Print/Typed) Gerald Meyer	Registration Number 41,194

[Page 2 of 2]

WARNING:

Petitioner/applicant is cautioned to avoid submitting personal information in documents filed in a patent application that may contribute to identity theft. Personal information such as social security numbers, bank account numbers, or credit card numbers (other than a check or credit card authorization form PTO-2038 submitted for payment purposes) is never required by the USPTO to support a petition or an application. If this type of personal information is included in documents submitted to the USPTO, petitioners/applicants should consider redacting such personal information from the documents before submitting them to the USPTO.

Petitioner/applicant is advised that the record of a patent application is submitted to the USPTO.

Petitioner/applicant is advised that the record of a patent application is available to the public after publication of the application (unless a non-publication request in compliance with 37 CFR 1.213(a) is made in the application) or issuance of a patent. Furthermore, the record from an abandoned application may also be available to the public if the application is authorization forms PTO-2038 submitted for payment purposes are not retained in the application.

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

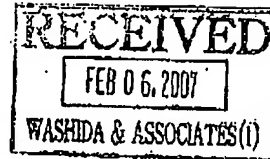
The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether disclosure of these records is required by the Freedom of Information Act.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspection or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

整理番号:7047980109 発送番号:049406 発送日:平成19年 2月 6日

1

拒絶理由通知書



特許出願の番号 特願2006-341650
起案日 平成19年 1月30日
特許庁審査官 桜井 茂行 2945 5M00
特許出願人代理人 鷺田 公一 様
適用条文 第29条第2項、第36条

この出願は、次の理由によって拒絶をすべきものである。これについて意見があれば、この通知書の発送の日から60日以内に意見書を提出して下さい。

理 由

理由 1

この出願の下記の請求項に係る発明は、その出願前に日本国内又は外国において、頒布された下記の刊行物に記載された発明又は電気通信回線を通じて公衆に利用可能となった発明に基いて、その出願前にその発明の属する技術の分野における通常の知識を有する者が容易に発明をすることができたものであるから、特許法第29条第2項の規定により特許を受けることができない。

記 (引用文献等については引用文献等一覽参照)

- ・請求項: 1-13
- ・引用文献: 1-3
- ・備考

引用文献1の第1、3-5図及びその説明にも記載されているように、機器からメモリデバイスへ、アクセス領域の処理命令と、当該処理命令に関する検証情報とを併せて送信し、メモリデバイスにおいて検証を行った後アクセス領域への処理を行うことは周知の技術である。

また、引用文献2の第3-4、12図(b)及びその説明、にも記載されているように、機器からメモリデバイスへ、領域番号・アクセス開始オフセット・データサイズ・を含むアクセス先指定情報を送信することも通常に行われていることであり、上記引用文献1において、引用文献2に記載されたアクセス先指定情報を送信してアクセス先を指定する構成とすることは当業者であれば容易に成し得る。

また、機器からメモリデバイスに領域番号を読み取るコマンドを用意し、領域番号をメモリデバイスから機器に送信する構成とすることも通常に行われている。

外国出願 No. 2F04119-PCT

整理番号:7047980109 発送番号:049406 発送日:平成19年 2月 6日 2

加えて、データ検証を行う通信において、機器とメモリデバイスが共有するセッション・キー（一時共有鍵、本願の機器とメモリデバイスが共有する検証用鍵に相当）を用いることも、引用文献3の第1-2図及びその説明にも記載されているように周知の技術である。データ検証に乱数を用いることも周知の技術である。

引用文献等一覧

1. 特開平11-306088号公報
2. 特開平6-302180号公報
3. 特開2001-118034号公報

理由2

この出願は、特許請求の範囲の記載が下記の点で、特許法第36条第6項第2号に規定する要件を満たしていない。

記

(1) 請求項4において、「前記指定情報を用いて前記検証情報の検証処理と、を行う指定情報検証手段と、」と記載されている。しかし、指定情報検証手段が行う処理は「検証情報の検証処理」のみしか記載されておらず、「と、」という表現は日本語として奇異である。

(2) 本願の発明の詳細な説明においては、アクセス領域を指定する指定情報にはアクセス開始オフセットとデータサイズが含まれ、これらの情報によって領域指定を行っている（本願の第20図等を参照）。しかし、請求項13において、指定情報には「アクセス開始オフセット」のみしか含まれておらず、どのように領域指定を行っているのか分からない。

また、請求項13において、指定情報に「アクセスするデータ」が含まれているが、何故アクセスするデータ自体が、アクセス領域を指定する指定情報に含まれているのか分からない。

よって、請求項4、13及び当該請求項を引用する請求項に係る発明は明確でない。

拒絶の理由が新たに発見された場合には拒絶の理由が通知される。

先行技術文献調査結果の記録

2007/11/14/本 11:40

FAX番号: 042-338-4305

P. 004

整理番号: 7047980109 発送番号: 049406 発送日: 平成19年 2月 6日 3/E

- ・ 調査した分野 G 0 6 F 1 2 / 1 4
G 0 6 K 1 7 / 0 0
- ・ 先行技術文献 特開 2 0 0 3 - 9 1 7 0 4 号公報

この先行技術文献調査結果の記録は拒絶理由を構成するものではありません。

(English Translation)

NOTICE OF REASON FOR REJECTION

Dispatch Date	February 6, 2007
Japanese Patent Application Number	2006-341650
Drafting Date	January 30, 2007
Examiner of Patent Office	Shigeyuki SAKURAI 2945 5M00
Attorney	Kimihito WASHIDA
Applied Provision	Section 29(2)

This application should be refused for the reason mentioned below. If the applicant has any argument against the reason, such argument should be submitted within 60 days from the date on which this notification was dispatched.

REASON

The inventions in the claims listed below of the subject application should not be granted a patent under the provision of Patent Law Section 29 (2) since they could have easily been made by persons who have common knowledge in the technical field to which the inventions pertain, on the basis of inventions described in the publications listed below which were distributed, or inventions made accessible to public through electric telecommunication line, in Japan or foreign countries prior to the filing of the subject application.

NOTE (see the list of cited references)

Claims: 1-13

Cited references: 1-3

Remarks:

As described in FIG. 1 and FIGs. 3-5 of the cited reference 1 and also in the descriptions thereof, it is a well known technique that a processing command of an access region and an authentication information regarding this processing command are transmitted together from an apparatus to a memory device, and after carrying out the authentication processing in the memory device the processing to the access region is carried out.

Moreover, as described in FIGs. 3-4, and FIG. 12 (b) of the cited reference 2 and also in the descriptions thereof, transmitting an access area designation information including the area number, access start offset, data size from an apparatus to a memory device is also usually carried out, and that in the above-described cited reference 1 the access area designation information described in the cited reference 2 is transmitted to designate the access destination can be easily done by those skilled in the art.

Moreover, a configuration of providing a command for reading the area number from the apparatus to the memory device and of transmitting the area number from the memory device to the apparatus is also commonly employed.

In addition, in the communications for carrying out data verification, the use of a session key (a temporarily shared key, and corresponding to a verification key which the apparatus and memory device of the subject application share) which the apparatus and the memory device share is also a well known technique as described in FIGs. 1 and 2 of the cited reference 3 and also in the descriptions thereof. The use of a random number in data verification is also a well known technique.

List of Cited Reference(s)

1. Japanese Patent Application Laid-Open No. HEI11-306088
2. Japanese Patent Application Laid-Open No. HEI6-302180
3. Japanese Patent Application Laid-Open No. 2001-118034

If any new reason for refusal is found in future, a further notice will be issued.

VERIFICATION OF A TRANSLATION

I, Takashi KISO
of 5th Floor, Shintoshicenter Bldg, 24-1, Tsurumaki 1-chome,
Tama-shi, Tokyo 206-0034 Japan

declare that I am well acquainted with both the Japanese and English
languages, and that the attached is an accurate translation, to the best of
my knowledge and ability, of the claim portion of Japanese Patent
Application No. 2006-341650 which is a Divisional Application of No.
2004-197453.

Signature:

Takashi Kiso

Takashi KISO

Date:

August 30, 2007

JP 3963938 B2 2007.8.22

(10) 日本国特許庁 (JP)

(12) 特 許 公 報 (B2)

(11) 特許番号

特許第3963938号

(P3963938)

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(51) Int. Cl.

F1

G06F 12/14 (2006.01)
G06K 17/00 (2006.01)
G06F 21/24 (2006.01)
G06K 19/073 (2006.01)
G06F 21/22 (2006.01)

G06F 12/14 510D
G06K 17/00 E
G06F 12/14 520A
G06K 19/00 P
G06F 9/06 680G

請求項の数 13 (全 46 頁)

(21) 出願番号 特願2006-341650 (P2006-341650)
(22) 出願日 平成18年12月19日 (2006.12.19)
(62) 分割の表示 特願2004-197453 (P2004-197453)
の分割
原出願日 平成16年7月2日 (2004.7.2)
(65) 公開番号 特開2007-133892 (P2007-133892A)
(43) 公開日 平成19年5月31日 (2007.5.31)
審査請求日 平成18年12月19日 (2006.12.19)
(31) 優先権主張番号 特願2003-275672 (P2003-275672)
(32) 優先日 平成15年7月16日 (2003.7.16)
(33) 優先権主張国 日本国 (JP)

早期審査対象出版

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審査官 核井 茂行

最終頁に続く

(54) 【発明の名称】 アクセス方法、メモリデバイス、および情報機器

(57) 【特許請求の範囲】

【請求項1】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記領域番号と、前記領域番号に関連づけられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記指定情報に基づいて生成した検証情報を、前記アクセス先への処理命令とともに送信するステップと、

前記メモリデバイスが、前記指定情報を用いて前記検証情報を検証するステップと、

前記検証にて成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと、を有するアクセス方法。

【請求項2】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記メモリデバイスとの間で、検証用鍵を共有化するステップと、

前記機器が、前記領域番号と、前記領域番号に関連づけられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記指定情報を前記検証用鍵で暗号化した検証データを、前記アクセス領

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域への処理命令とともに送信するステップと、

前記メモリデバイスが、前記指定情報と前記検証用鍵とを用いて、前記検証データを検証するステップと、

前記検証にて成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと、

を有するアクセス方法。

【請求項3】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記メモリデバイスとの間で、検証用鍵を共有化するステップと、

前記機器が、前記メモリデバイスに乱数の送信を要求するステップと、

前記メモリデバイスが、前記機器に対して乱数を送信するステップと、

前記機器が、前記領域番号と、前記領域番号と関連付けられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記乱数と前記指定情報とを含む検証情報を前記検証用鍵で暗号化した検証データを前記アクセス先への処理命令とともに送信するステップと、

前記メモリデバイスが、前記乱数と前記指定情報と前記検証用鍵とを用いて、前記検証データを検証するステップと、

前記検証にて成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと、

を有するアクセス方法。

【請求項4】

機器から、領域番号に割り当てられた特定の領域のデータが読み書きされるメモリデバイスであって、

前記領域番号と、前記領域番号と関連付けられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、アクセス先を指定する指定情報を受信するとともに、前記指定情報に基づいて生成された検証情報と読み出し又は書き込み命令を含む処理命令を受信する処理命令受信手段と、

前記指定情報を用いて前記検証情報の検証処理を行う指定情報検証手段と、

データを格納する記憶領域と、

前記検証処理が成功した場合に、前記処理命令に応じて、前記記憶領域の前記指定領域に対する読み出し又は書き込みを行う記憶領域アクセス手段と、

前記記憶領域アクセス手段が読み出したデータを前記機器に送信するデータ送信手段と

前記機器から書き込みデータを受信するデータ受信手段と、

を備えるメモリデバイス。

【請求項5】

前記指定情報検証手段は、前記機器の要求により、乱数を生成し、前記乱数を保持し、前記乱数を前記機器に送信する、

請求項4記載のメモリデバイス。

【請求項6】

前記指定情報検証手段は、前記検証処理を、前記検証情報と検証用鍵を用いて行う、

請求項5記載のメモリデバイス。

【請求項7】

前記機器との間で前記検証用鍵を共有する検証用鍵共有手段をさらに備える、請求項6記載のメモリデバイス。

【請求項8】

メモリデバイスに対して領域番号に割り当てられた特定の領域のデータの読み書きする情報機器であって、

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前記特定の領域における、データの読み出し又は書き込みするアクセス開始オフセットと、読み出し又は書き込みするデータのサイズと、前記領域番号と、を含む、アクセス先を指定する指定情報を決定する指定情報決定手段と、

前記指定情報に基づいて前記検証情報の生成処理を行う検証情報生成手段と、
前記指定情報を前記メモリデバイスに送信するとともに、別途、前記検証情報と、データの読み出し又は書き込みの処理命令と、を送信する処理命令送信手段と、
前記処理命令が書き込みの場合は、前記メモリデバイスにデータを送信するデータ送信手段と、

前記処理命令が読み出しの場合は、前記メモリデバイスからデータを受信するデータ受信手段と、
を備える情報機器。

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【請求項 9】

前記検証情報生成手段は、前記メモリデバイスに対し乱数の送信を要求し、前記メモリデバイスから前記乱数を受信する、

請求項 8 記載の情報機器。

【請求項 10】

前記検証情報生成手段は、前記検証情報の生成処理を、前記指定情報と検証用鍵とを用いて行う、

請求項 9 記載の情報機器。

【請求項 11】

前記メモリデバイスとの間で前記検証用鍵を共有する検証用鍵共有手段を備える、

請求項 10 記載の情報機器。

【請求項 12】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記領域番号と、前記領域番号と関連付けられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記指定情報を検証用鍵で暗号化した検証データを前記アクセス先への処理命令とともに前記メモリデバイスへ送信するステップと、

前記メモリデバイスが、前記検証データを前記指定情報と検証用鍵とを用いて検証するステップと、

前記検証に成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと

を有するアクセス方法。

【請求項 13】

機器からメモリデバイスの領域番号に割り当てられた特定の領域に対するアクセス方法であって、

前記機器が、前記メモリデバイスに乱数の送信を要求するステップと、

前記メモリデバイスが、前記機器に対して乱数を送信するステップと、

前記機器が、前記領域番号と、前記領域番号と関連付けられた前記特定の領域内におけるアクセス開始オフセットと、アクセスするデータのサイズと、を含む、前記メモリデバイスへのアクセス先を指定する指定情報を前記メモリデバイスへ送信するステップと、

前記機器が、前記指定情報を検証用鍵で暗号化した検証データを前記アクセス先への処理命令と併せて前記メモリデバイスへ送信するステップと、

前記メモリデバイスが、前記乱数と前記指定情報と前記検証用鍵を用いて前記検証データを検証するステップと、

前記検証に成功した場合、前記メモリデバイスが、前記処理命令を実行するステップと

を有するアクセス方法。

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1 ページ

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Inventor:

Applicant:

Classification:

- International: G06F12/14; G06F21/22; G06F21/24; G06K17/00; G06K19/073;
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- European:

Application number: JP20060341650 20061219

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Abstract not available for JP2007133892

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esp@cenet Family list view

1 ページ

Family list

8 family members for: JP2007133892
Derived from 8 applications

1. Access method
Inventor: TAKAFUMI TAKAGI YOSHIHIKO KIKU (JP) Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP)
EC: IPC: G06F12/14; G06F12/14; (IPC1-7): G06F12/14
Publication info: CN1701310 A - 2005-11-23
2. ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO (JP); KIKUCHI TAKAFUMI (JP) Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP)
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+16)
Publication info: EP1560120 A1 - 2005-08-03
3. ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO; KIKUCHI TAKAFUMI Applicant: MATSUSHITA ELECTRIC IND CO LTD
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+18)
Publication info: JP2005050320 A - 2005-02-24
4. No title available
Inventor: Applicant:
EC: IPC: G06F12/14; G06F21/22; G06F21/24 (+6)
Publication info: JP2007133892 A - 2007-05-31
5. ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO (JP); KIKUCHI TAKAFUMI (JP) Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP)
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+14)
Publication info: KR20060024317 A - 2006-03-16
6. ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO (JP); KIKUCHI TAKAFUMI (JP) Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP)
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F12/00; G06F21/00 (+16)
Publication info: KR20070009740 A - 2007-01-18
7. Access method
Inventor: TAKAGI YOSHIHIKO (JP); KIKUCHI TAKAFUMI (JP) Applicant:
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+15)
Publication info: US2005246546 A1 - 2005-11-03
8. ACCESS METHOD
Inventor: TAKAGI YOSHIHIKO; KIKUCHI TAKAFUMI Applicant: MATSUSHITA ELECTRIC IND CO LTD (JP);
TAKAGI YOSHIHIKO; (+1)
EC: G06F21/00N1D; G06F21/00N9A; (+3) IPC: G06F12/14; G06F21/00; G06F21/06 (+16)
Publication info: WO2005008502 A1 - 2005-01-27

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[SCOPE OF CLAIMS FOR PATENT]

[Claim 1]

An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification information generated based on the designation information, together with a processing command for the access area;

the memory device verifying the verification information using the designation information; and

the memory device executing the processing command when the verification succeeds.

20 [Claim 2]

An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus sharing a verification key with the memory device;

the apparatus transmitting designation information including the area number, an access start

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offset in the specific area associated with the area number
and a size of data to access and designating an access
area of the memory device, to the memory device;

the apparatus transmitting verification data
5 obtained by encrypting the designation information using
the verification key, together with a processing command
for the access area;

the memory device verifying the verification data
using the designation information and the verification
10 key; and

the memory device executing the processing command
when the verification succeeds.

[Claim 3]

15 An access method for an apparatus to gain access
to a specific area assigned to an area number of a memory
device, the method comprising the steps of:

the apparatus sharing a verification key with the
memory device;

20 the apparatus requesting transmission of random
numbers to the memory device;

the memory device transmitting random numbers to
the apparatus;

the apparatus transmitting designation
25 information including the area number, an access start
offset in the specific area associated with the area number
and a size of data to access and designating an access

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area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting verification information including the random numbers and the designation information, together with a processing command for the access area;

the memory device verifying the verification data using the random numbers, the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.

[Claim 4]

A memory device whose data of a specific area assigned to an area number is read and written by an apparatus, the memory device comprising:

a processing command receiver that receives designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an area to access and receives verification information generated based on the designation information and a processing command including a command for read or write;

a designation information verifier that performs verification processing on the verification information using the designation information;

a storage area that stores data;

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a storage area access section that performs read or write from/in a designated area of the storage area according to the command for processing, when the verification processing succeeds;

5 a data transmitter that transmits data read by the storage area access section to the apparatus; and

a data receiver that receives data to write from the apparatus.

10 [Claim 5]

The memory device according to claim 4, wherein the designation information verifier generates random numbers in response to a request by the apparatus, holds the random numbers and transmits the random numbers to
15 the apparatus.

[Claim 6]

The memory device according to claim 5, wherein the designation information verifier performs the
20 verification processing using the verification information and a verification key.

[Claim 7]

The memory device according to claim 6, further
25 comprising:

a verification key sharing section that shares the verification key with the apparatus.

[Claim 8]

An information apparatus that reads and writes data of a specific area assigned to an area number from/in a memory device, the information apparatus comprising:

5 a designation information determiner that determines designation information including an access start offset for reading and writing data from/in the specific area, a size of data for performing read and write and the area number and designating the access area;

a verification information generator that performs processing for generating verification information based on the designation information;

15 a processing command transmitter that transmits the designation information to the memory device and separately transmits the verification information and a processing command for reading or writing data;

a data transmitter that transmits data to the memory device when the processing command is write; and

20 a data receiver that receives data from the memory device when the processing command is read.

[Claim 9]

The information apparatus according to claim 8, wherein the detection information verifier requests transmission of random numbers to the memory device and receives the random numbers from the memory device.

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[Claim 10]

The information apparatus according to claim 9, wherein the verification information generator performs the processing for generating the verification information using the designation information and a verification key.

[Claim 11]

The information apparatus according to claim 10, further comprising:

a verification key sharing section that shares the verification key with the memory device.

[Claim 12]

An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising:

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting the designation information using the verification key, together with a processing command for the access area to the memory device;

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the memory device verifying the verification data using the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.

[Claim 13]

An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus requesting transmission of random numbers to the memory device;

the memory device transmitting random numbers to the apparatus;

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting the designation information using a verification key, together with a processing command for the access area to the memory device;

the memory device verifying the verification data using the random numbers, the designation information and the verification key; and

the memory device executing the processing command

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when the verification succeeds.

Attorney Docket No. 2664211

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

TAKAGI et al.

Confirmation No. 6449

Application No. 10/527,820

Group Art Unit: 2136

Filed: May 11, 2005

Examiner: Unknown

Title: ACCESS METHOD

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith for filing in the U.S. Patent and Trademark Office is the following:

- (1) Transmittal Letter; and
- (2) Supplemental Preliminary Amendment.

Respectfully submitted,
THE NATH LAW GROUP

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Appl. No. 10/527,820
Supplemental Preliminary Amendment

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

TAKAGI et al.

Confirmation No. 6449

Application No. 10/527,820

Group Art Unit: 2136

Filed: May 11, 2005

Examiner: Unknown

Title: ACCESS METHOD

SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Commissioner:

Prior to examining the above application on the merits, please enter the following amendments to the claims.

Amendments to the Claims begin on page 2 of this paper.

Appl. No. 10/527,820
Supplemental Preliminary Amendment

Amendments to the Claims:

Claims 1-19 (Canceled).

20. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:
- the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;
 - the apparatus transmitting verification information generated based on the designation information, together with a processing command for the access area;
 - the memory device verifying the verification information using the designation information; and
 - the memory device executing the processing command when the verification succeeds.
21. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:
- the apparatus sharing a verification key with the memory device;
 - the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;
 - the apparatus transmitting verification data obtained by encrypting the designation information using the verification key, together with a processing command for the access area;

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Supplemental Preliminary Amendment

the memory device verifying the verification data using the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.

3 22. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:

the apparatus sharing a verification key with the memory device;

the apparatus requesting transmission of random numbers to the memory device;

the memory device transmitting random numbers to the apparatus;

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting verification information including the random numbers and the designation information, together with a processing command for the access area;

the memory device verifying the verification data using the random numbers, the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.

4 23. (New) A memory device whose data of a specific area assigned to an area number is read and written by an apparatus, the memory device comprising:

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Supplemental Preliminary Amendment

a processing command receiver that receives designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an area to access and receives verification information generated based on the designation information and a processing command including a command for read or write;

a designation information verifier that performs verification processing on the verification information using the designation information;

a storage area that stores data;

a storage area access section that performs read or write from/in a designated area of the storage area according to the command for processing, when the verification processing succeeds;

a data transmitter that transmits data read by the storage area access section to the apparatus; and

a data receiver that receives data to write from the apparatus.

24. (New) The memory device according to claim 23, wherein the designation information verifier generates random numbers in response to a request by the apparatus, holds the random numbers and transmits the random numbers to the apparatus.

25. (New) The memory device according to claim 24, wherein the designation information verifier performs the verification processing using the verification information and a verification key.

26. (New) The memory device according to claim 25, further comprising:

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Supplemental Preliminary Amendment

a verification key sharing section that shares the verification key with the apparatus.

27. (New) An information apparatus that reads and writes data of a specific area assigned to an area number from/in a memory device, the information apparatus comprising:

a designation information determiner that determines designation information including an access start offset for reading and writing data from/in the specific area, a size of data for performing read and write and the area number and designating the access area;

a verification information generator that performs processing for generating verification information based on the designation information;

a processing command transmitter that transmits the designation information to the memory device and separately transmits the verification information and a processing command for reading or writing data;

a data transmitter that transmits data to the memory device when the processing command is write; and

a data receiver that receives data from the memory device when the processing command is read.

28. (New) The information apparatus according to claim 27, wherein the detection information verifier requests transmission of random numbers to the memory device and receives the random numbers from the memory device.

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Supplemental Preliminary Amendment

29. (New) The information apparatus according to claim 28, wherein the verification information generator performs the processing for generating the verification information using the designation information and a verification key.
30. (New) The information apparatus according to claim 29, further comprising:
a verification key sharing section that shares the verification key with the memory device.
31. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising:
the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;
the apparatus transmitting verification data obtained by encrypting the designation information using the verification key, together with a processing command for the access area to the memory device;
the memory device verifying the verification data using the designation information and the verification key; and
the memory device executing the processing command when the verification succeeds.
32. (New) An access method for an apparatus to gain access to a specific area assigned to an area number of a memory device, the method comprising the steps of:
the apparatus requesting transmission of random numbers to the memory device;
the memory device transmitting random numbers to the apparatus;

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Supplemental Preliminary Amendment

the apparatus transmitting designation information including the area number, an access start offset in the specific area associated with the area number and a size of data to access and designating an access area of the memory device, to the memory device;

the apparatus transmitting verification data obtained by encrypting the designation information using a verification key, together with a processing command for the access area to the memory device;

the memory device verifying the verification data using the random numbers, the designation information and the verification key; and

the memory device executing the processing command when the verification succeeds.